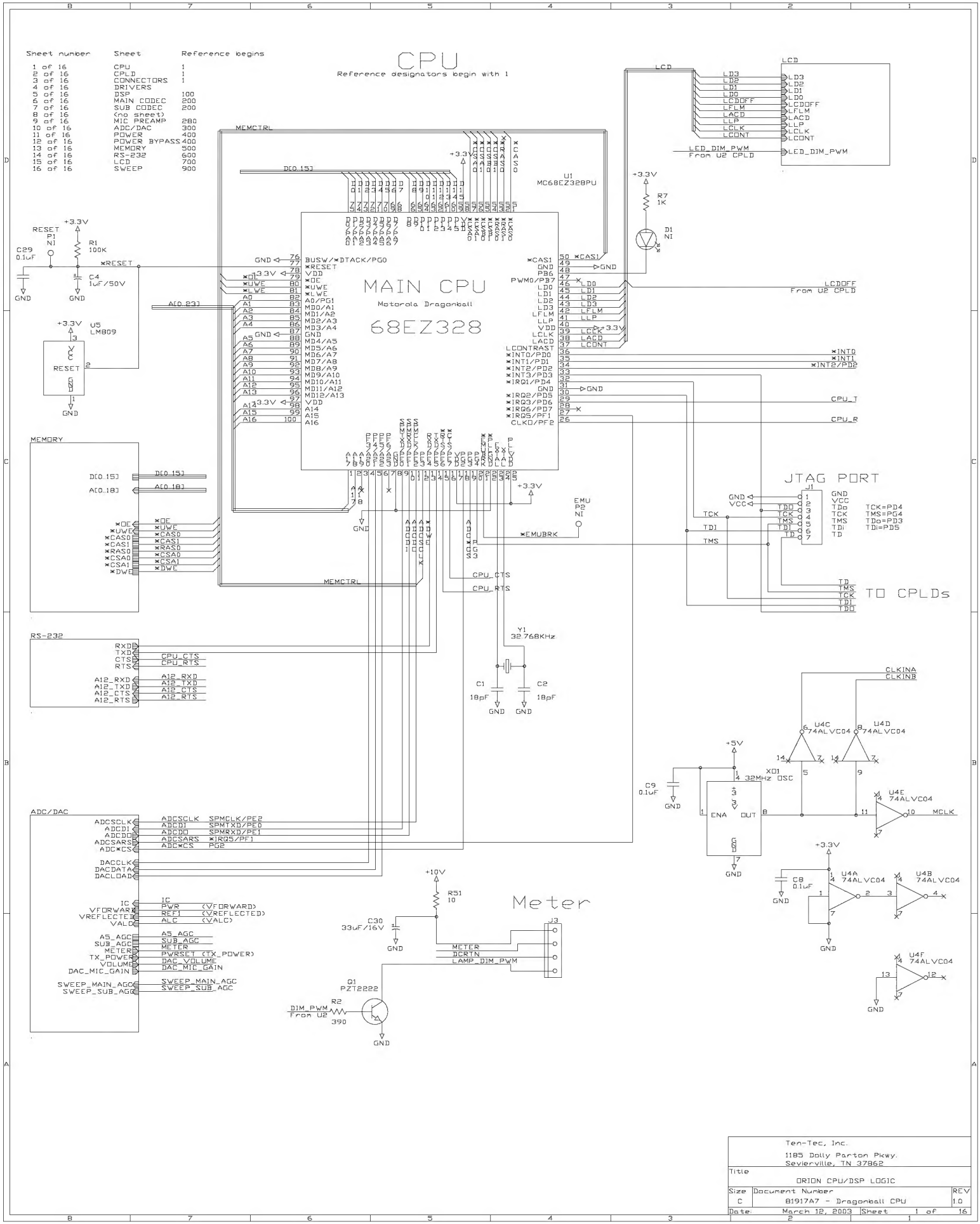
Sheet number Sheet Reference begins
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2 of 16 CPLD 1
3 of 16 CONNECTORS 1
4 of 16 DRIVERS
5 of 16 DSP 100
6 of 16 MAIN CODEC 200
7 of 16 SUB CODEC 200
8 of 16 (no sheet)
9 of 16 MIC PREAMP 280
10 of 16 ADC/DAC 300
11 of 16 POWER 400
12 of 16 POWER BYPASS 400
13 of 16 MEMORY 500
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CPU
Reference designators begin with 1
MAIN CPU
Motorola Dragonball
68EZ328
U1
MC68EZ328PU
MEMCTRL
LCD
LED_DIM_PWM
From U2 CPLD
LCDOFF
From U2 CPLD
JTAG PORT
TO CPLDs
MEMORY
RS-232
ADC/DAC
Meter
Y1
32.768KHz
CLKINA
CLKINB
MCLK
Ten-Tec, Inc.
1185 Dolly Parton Pkwy.
Sevierville, TN 37862
Title
ORION CPU/DSP LOGIC
Size C
Document Number
81917A7 - Dragonball CPU
REV 1.0
Date: March 12, 2003
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